

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING - DATA SCIENCE

Lesson Plan & Work-done Diary for AY: 2026-27, ODD Semester

Course with Code: Digital Design and Computer Organization -1BCS303				Faculty: Mr. J N Karthik			Semester & Section: III	
Class No.	Date planned (DD/MM)	Topics to be covered	TLP Planned	Class No.	Date of Conduction (DD/MM)	Topics Covered	TLP Executed	Remarks if any deviation
MODULE-1								
1.		Introduction to Digital Design and Computer Organization	Chalk & Talk PPT	1				
2.		Binary Logic	Chalk & Talk PPT	2				
3.		Basic Theorems and Properties of Boolean Algebra	Chalk & Talk PPT	3				
4.		Boolean Functions	Chalk & Talk PPT	4				
5.		Digital Logic Gates	Chalk & Talk PPT	5				
6.		Integrated Circuits.	Chalk & Talk PPT	6				
7.		Gate-Level Minimization	Chalk & Talk PPT	7				
8.		The Map Method	Chalk & Talk PPT	8				
9.		Four-Variable Map,	Chalk & Talk PPT	9				
10		Product of sums simplification	Chalk & Talk PPT	10				
11		Don't-Care Conditions	Chalk & Talk PPT	11				
12.		NAND and NOR Implementation	Chalk & Talk PPT	12				



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MODULE-2								
1.		Combinational Logic: Introduction	Chalk & Talk PPT	1				
2.		Combinational Circuits,	Chalk & Talk PPT	2				
3.		Analysis of Combinational Circuits	Chalk & Talk PPT	2				
4.		Design Procedure	Chalk & Talk PPT	4				
5.		Binary Adder – Subtractor, Decoders	Chalk & Talk PPT	5				
6.		Encoders	Chalk & Talk PPT	6				
7.		Multiplexers	Chalk & Talk PPT	7				
8.		Sequential Logic: Introduction	Chalk & Talk PPT	8				
9.		Sequential Circuits	Chalk & Talk PPT	9				
10.		Storage Element	Chalk & Talk PPT	10				
11.		Latches	Chalk & Talk PPT	11				
12.		Flip-Flops	Chalk & Talk PPT	12				

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MODULE-3								
1.		Basic Structure of Computers: Functional Units	Chalk & Talk PPT	1				
2.		Basic Operational Concepts	Chalk & Talk PPT	2				
3.		Bus structure, Performance – Processor Clock	Chalk & Talk PPT	3				
4.		Basic Performance Equation, Clock Rate	Chalk & Talk PPT	4				
5.		Instruction Set: CISC an RISC, Performance Measurement.	Chalk & Talk PPT	5				
6.		Machine Instructions and Programs	Chalk & Talk PPT	6				
7.		Memory Location and Addresses	Chalk & Talk PPT	7				
8.		Memory Operations	Chalk & Talk PPT	8				
9.		Instruction and Instruction sequencing	Chalk & Talk PPT	9				
10.		Addressing Modes.	Chalk & Talk PPT	10				

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MODULE-4								
1.		Input/output Organization	Chalk & Talk PPT	1				
2.		Accessing I/O Devices	Chalk & Talk PPT	2				
3.		Interrupts – Interrupt Hardware	Chalk & Talk PPT	3				
4.		Enabling and Disabling Interrupts	Chalk & Talk PPT	4				
5.		Handling Multiple Devices	Chalk & Talk PPT	5				
6.		Direct Memory Access, Buses	Chalk & Talk PPT	6				
7.		The Memory System: Some Basic Concepts	Chalk & Talk PPT	7				
8.		Semiconductor RAM Memories	Chalk & Talk PPT	8				
9.		Read Only Memory	Chalk & Talk PPT	9				
10.		Speed, size and Cost of memory systems.	Chalk & Talk PPT	10				
11.		Cache Memories – Mapping Functions.	Chalk & Talk PPT	11				
12.		Revision	Chalk & Talk PPT	12				

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MODULE-5								
1.		Basic Processing Unit: Some Fundamental Concepts	Chalk & Talk PPT	1				
2.		Execution of a Complete Instruction	Chalk & Talk PPT	2				
3.		Multiple Bus Organization	Chalk & Talk PPT	3				
4.		Hardwired Control.	Chalk & Talk PPT	4				
5.		Pipelining: Basic concepts	Chalk & Talk PPT	5				
6.		Role of Cache memory	Chalk & Talk PPT	6				
7.		Pipeline Performance	Chalk & Talk PPT	7				
8.		Revision	Chalk & Talk PPT	8				
9.		Revision	Chalk & Talk PPT	9				
10.		Quiz	MS Forms	10				

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	Activity	Planned	Actual	Remarks
1	Theory Classes	56		
2	Assignments/ Quizzes/Self-study	3		
3	Tutorials/ Extra classes	1		
4	Internal Assessments	3		
5	ICT based Teaching (% of usage in Curriculum)	100		
Planning			Execution	
Faculty Signature:			Faculty Signature:	
HoD Signature:			HoD Signature:	