



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Lesson Plan & Work-done Diary for AY: 2026-27 ODD Semester

Course with Code: Analog Electronics and Linear Integrated Circuits (1BEC304)				Faculty Name: Mr. Guruprasad K N			Semester: 3 rd A sec	
Class No.	Date planned	Topics to be covered	TLP Planned	Class No.	Date of Conduction	Topics Covered	TLP Executed	Remarks if any deviation
MODULE-0								
1.		Fundamentals of Semiconductor Devices	Chalk &Talk					
2.		Knowledge of Circuit Analysis	Chalk &Talk					
3.		Basic Electronic Components	Chalk &Talk					



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MODULE-5								
4.		Linear Op-amp Circuits: Digital to Analog Conversion: Weighted Resistor DAC, R-2R DAC	PPT					
5.		Analog to Digital Conversion: Digital RAMP ADC	PPT					
6.		Successive Approximation ADC	PPT					
7.		Signal Processing Circuits: Saturating and Non Saturating Precision HWR	Chalk &Talk					
8.		Precision FWR Using HWR and Summing Circuit	Chalk &Talk					
9.		Nonlinear Op-Amp Circuits: Zero Crossing Detector	PPT					
10.		Inverting Schmitt Trigger Circuit, Non-Inverting Schmitt Trigger Circuit.	Chalk &Talk					
11.		DC Voltage Regulators: Introduction, Voltage Regulator Basics.	Chalk &Talk					

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MODULE-1								
12.		BJT AC Models: Base Biased Amplifier, Emitter Biased Amplifier	Chalk &Talk					
13.		Small Signal Operation,	Chalk &Talk					
14.		AC Beta, AC Resistance of The Emitter Diode	Chalk &Talk					
15.		Two Transistor Models, Analyzing Amplifier	Chalk &Talk					
16.		Voltage Amplifiers: Voltage Gain, Multistage Amplifiers.	PPT					
17.		CC Amplifier, Output Impedance	PPT					
18.		Cascading CE And CC, Darlington Connections	PPT					
19.		Voltage Regulation	Chalk &Talk					
20.		The Common Base Amplifier.	Chalk &Talk					



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MODULE-2								
21.		MOSFET Biasing in MOS Amplifier Circuits: Fixing VGS	Chalk &Talk					
22.		Fixing VG, Drain to Gate Feedback Resistor.	Chalk &Talk					
23.		The DC Bias Point, Signal Current in Drain	Chalk &Talk					
24.		Voltage Gain, Small Signal Equivalent Circuit Models	Chalk &Talk					
25.		Transconductance, The T Equivalent Circuit Model.	Chalk &Talk					
26.		Basic Configurations, Characterizing Amplifiers	Chalk &Talk					
27.		CS Amplifier with And Without Source Resistance,	Chalk &Talk					
28.		The Common Gate Amplifier,	Chalk &Talk					
29.		Source Follower	Chalk &Talk					



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MODULE-3								
30.		Four Types of Negative Feedback, VCVS Voltage Gain	PPT					
31.		Other VCVS Equations, ICVS Amplifier	PPT					
32.		VCIS Amplifier, ICIS Amplifier	Chalk &Talk					
33.		Theory of Sinusoidal Oscillation, The Wein-Bridge Oscillator,	PPT					
34.		RC Phase Shift Oscillator, The Colpitts Oscillator	PPT					
35.		Hartley Oscillator, Crystal Oscillator.	PPT					
36.		The 555 Timer: Monostable Operation	PPT					
37.		Astable Operation.	PPT					

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MODULE-4								
38.		Power Amplifiers: Amplifier Terms, Two Load Lines,	PPT					
39.		Class A Operation, Class B Operation	PPT					
40.		Class B Push Pull Emitter Follower, Class C Operation	PPT					
41.		Active Filters: Ideal Responses, First Order Stages	PPT					
42.		VCVS Unity Gain Second Order Low Pass Filters,	PPT					
43.		VCVS Equal Component Low Pass Filters	PPT					
44.		VCVS High Pass Filters	PPT					
45.		MFB Bandpass Filters, Bandstop Filters.	PPT					



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Sl. No	Activity	Planned	Actual	Remarks
1	Theory Classes	45		
2	Assignments/ Quizzes	5		
3	Tutorials/ Extra classes	-		
4	Internal Assessments	3		
5	ICT based Teaching (% of usage in Curriculum)	60		
Planning			Execution	
Faculty Signature:			Faculty Signature:	
HoD Signature:			HoD Signature:	